# **idaq-871 iDAQ-873**

# 4-ch, 24-bit, 25.6kS/s, Full/Half/ Quarter Bridge Input iDAQ module

8-ch, 24-bit, 25.6kS/s, Quarter Bridge Input iDAQ module



## **Features**

- High resolution (24-bit)
- All types of bridges supported (full, half and quarter)
- Software-configurable and multiple bridge completion flexibility (iDAQ-871)
- Synchronization along with other iDAQ modules
- Multiple trigger modes for flexible configured acquisition timing
- Built-in digital filter for higher SNR

# Introduction

The iDAQ-871/873 series are designed for bridge-type high-precision data acquisition. There's built-in Wheatstone bridge ready in the measurement channels. The built-in digital filter could also filter down most of the unnecessary noises, which enhanced the measurement accuracy. It's suitable for measurement from sensors like strain gauges and load cells.

# **Specifications**

#### **Bridge Input**

Item		idaq-871			iDAQ-873	
ADC Resolution		24-bit				
Channels		4			8	
Input Range		±1 V/V, ±500 mV/V, ±250 mV/V, ±125 mV/V, ±62.5 mV/V, ±31.25 mV/V, ±15.63 mV/V, or ±7.81 mV/V				
Bridge Mode		Full, half, quarter			Quarter	
Bridge Resistance		120 Ω, 350 Ω, 1 kΩ			120 Ω	
Shunt Calibration		100 kΩ				
Excitation Voltage		2.5V, 5	2.5V, 5V		2V	
<ul> <li>Isolation protection</li> <li>Accuracy <sup>(1)</sup></li> </ul>		to user manual for detail information -3 dB bandwidth: Configured along with sampling rate. Refer to user manual for detail information 600 V <sub>DC</sub> , Channel to FGND Operating temperature within $\pm 5^{\circ}$ C of last, system-calibration temperature (25°C)				
	stem Calibration Temp. Over System Calibration Temp.					
Items	Gain		Offset	Gain		Offset
Calibrated	±0.05%		±0.05%	±0.20%		±0.25%
Uncalibrated (2)	±0.20%		±0.20%	±0.70%		±0.35%
<ul> <li>Shunt calibration</li> <li>Over-voltage protection</li> <li>Acquisition type</li> <li>Buffered acquisition</li> </ul>		Resistance: 100 kΩ Resistance accuracy: ±0.1% max. Resistance drift: ±100 ppm/°C max. ±30 V Instant or buffered, software configurable Enabled channel combination: Each channel can be enabled/disabled independently by software Sample clock rate: 25.6 kHz max., for all channels, simultaneous sampling, software configurable				
Trigger • Number of triggers • Trigger action • Trigger delay range • Sample number		2 max., selectable via software Start, delay to start, stop, or delay to stop 0 ~ 16,777,215 samples 0 ~ 16,777,215 samples				

(1) For detail specification, please refer to the user manual.

<sup>(2)</sup> System calibration should be taken place before starting the measurement whenever new measurement system is built, otherwise the accuracy would stay uncalibrated.

#### **Analog Trigger**

- Source
- Input range
- Resolution Hysteresis
- Accuracy
- Polarity
- Minimum width

#### **Digital Trigger**

# Source

- **Physical** Form factor
- Dimension
- Weight

- Operating temperature
  - Storage temperature -40 °C to 85 °C (-40 °F to 185 °F) Operating humidity
    - Up to 90% RH, non-condensing 5Grms, random vibration
- Vibration Shock
- 30G EMC: CE, FCC Certification

# **Ordering Information**

- iDAQ-871
- 4-ch, 24-bit, 25.6kS/s, Full/Half/Quarter Bridge Input iDAQ module 8-ch, 24-bit, 25.6kS/s, Quarter Bridge Input iDAQ module

### Accessories

IDAQ-873

- ADAM-3937-AE PCL-10137-1E
- PCL-10137-2E PCL-10137-3E
- DB-37 Wiring Terminal, DIN-rail Mount DB-37 Shielded Cable, 1m DB-37 Shielded Cable, 2m DB-62 Shielded Cable 3m

AD\ANTECH

2 external input sources, via iDAQ chassis/module iDAQ Module 175 g 37-pin D-SUB

100 x 80 x 25 mm (3.94 x 3.15 x 0.98 in.)

Full scale of analog input range

±0.01% of full-scale range max.

24-bit

1/(sample rate)

One of the analog input channels, software configurable

1/256 of analog input range, software configurable

Rising edge or falling edge, software configurable

I/O connector

Environmental

-40 ~ 70 °C (-40 ~158 °F)